



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of: **Mitsushi FUJIKI**

Group Art Unit: **2823**

Application Number: **10/772,253**

Examiner: **Thanh V. Pham**

Filed: **February 6, 2004**

Confirmation Number: **6491**

For: **METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE  
INCLUDING A LOW-TEMPERATURE LOWER ELECTRODE-  
FORMING STEP (As herein amended)**

Attorney Docket Number: **042068**  
Customer Number: **38834**

**AMENDMENT UNDER 37 C.F.R. §1.111**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

October 31, 2005

Sir:

In response to the Office Action dated June 30, 2005, the response date of which having been extended by one month to October 30, 2005 (Sunday), Applicants amend the specification and submit the following remarks.

Amendments to the Specification begin on page 2 of this paper.

Remarks begin on page 3 of this paper.